

Fig. 1

Device Capability Table

|         |              |                               |
|---------|--------------|-------------------------------|
| Node #0 | Reception    | ATSC-HD/SD, DSS-HD, Analog    |
|         | Transmission | None                          |
|         | Conversion   | None                          |
| Node #1 | Reception    | DSS-HD/SD                     |
|         | Transmission | DSS-HD/SD, Analog             |
|         | Conversion   | DSS-HD/SD -> Analog           |
| Node #2 | Reception    | ATSC-HD/SD, DSS-HD/SD, Analog |
|         | Transmission | ATSC-HD/SD, DSS-HD/SD, Analog |
|         | Conversion   | None                          |
| Node #3 | Reception    | DV-SD, Analog                 |
|         | Transmission | DV-SD, Analog                 |
|         | Conversion   | None                          |
| Node #4 | Reception    | DV-SD                         |
|         | Transmission | ATSC-SD                       |
|         | Conversion   | DV-SD -> ATSC-SD              |

Fig. 2

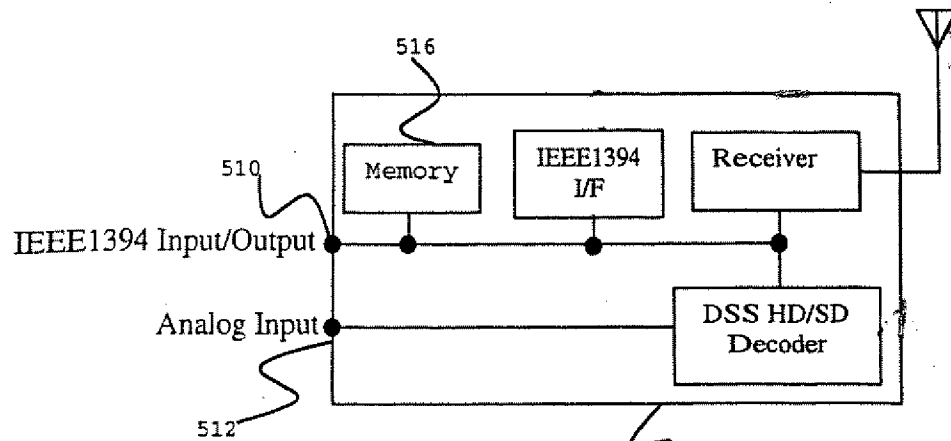


Figure 5

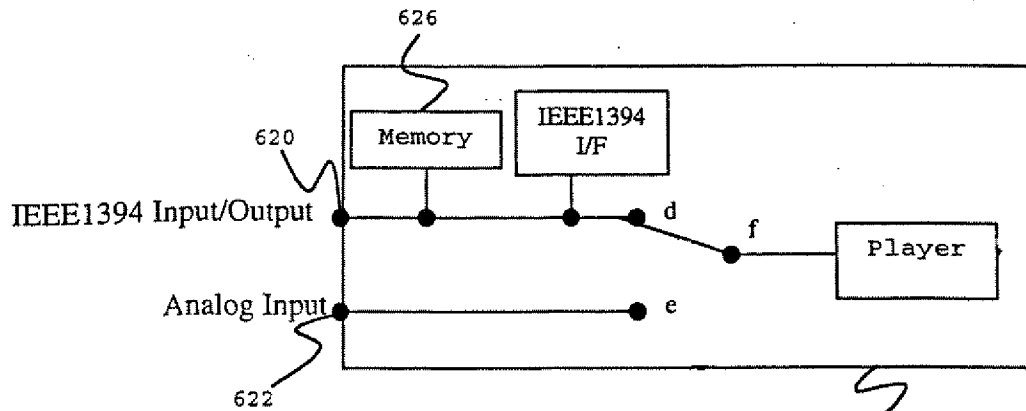


Figure 6

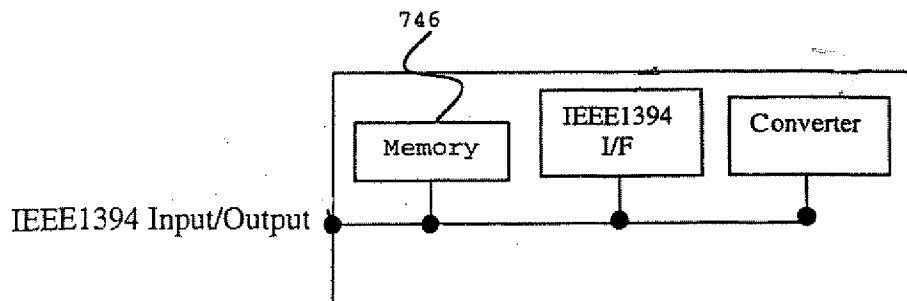


Figure 7

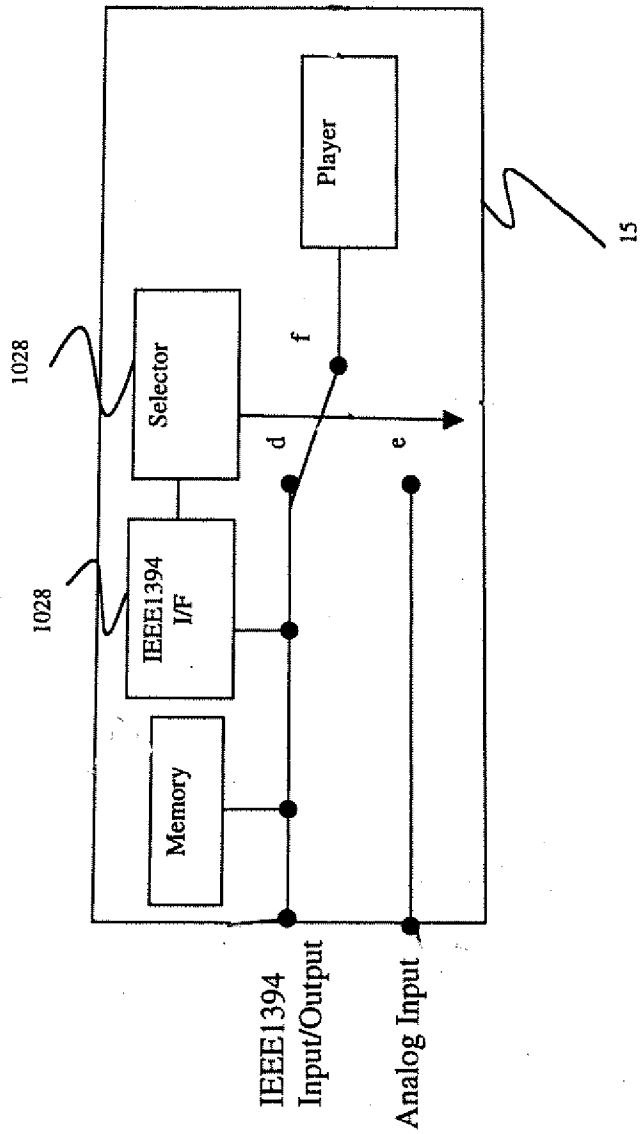


Figure 10